



PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): John S. Yates, Jr., et al.

Serial No.: 09/429,094

Filed: October 28, 1999

Title: SIDE TABLES ANNOTATING AN INSTRUCTION STREAM

Art Unit: 2783

Examiner: D. Eng

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COMMISSIONER FOR PATENTS
Washington D.C. 20231

Technology Center 2100

SUPPLEMENTAL AMENDMENT

After entering the Response to Office Action filed August 10, 2001, kindly further amend the application as follows.

In the claims:

11. (twice amended) The microprocessor chip of claim 10, further comprising:
a binary translator programmed to translate at least a selected portion of a computer program from a first binary representation to a second binary representation; and
wherein [within] the pipeline control circuitry is further designed to initiate a determination of whether to transfer control from an execution of the instruction, the instruction being an instruction of the first binary representation of the program, to the second binary representation, and effective to initiate the determination with neither a query nor a transfer of control to the second binary representation being coded into the first binary representation.

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Response to Restriction Requirement

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September 19, 2001

Reference No. 30585/16

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I hereby certify that the attached

- Clean Copy of Claims as Amended August 21, 2001
- along with any paper(s) referred to as being attached or enclosed) are being transmitted on September 19, 2001 to the Commissioner for Patents, Washington, D.C. 20221.

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